HYPERLABS
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HL9405 Broadband Balun (50 GHz)

Features and Technical Specifications

Bandwidth (-3 dB)	500 kHz to 50 GHz
Amplitude Match	± 0.1 dB to 30 GHz ± 0.25 dB to 50 GHz See *Fig. 1* below
Phase Match	± 4-6° at 20 GHz ± 8-12° at 40 GHz
Rise time	< 7 ps
Insertion Delay	≈ 278 ps
Insertion Loss	-6 dB
Return Loss	See *Figs. 3-4* below
VSWR	See *Fig. 5* below
CMRR	> 70 dB at 10 MHz > 35 dB at 50 GHz
Eye Diagrams	See Figs. 8-13 below
Max Input Power	+30 dBm
Impedance	50 Ω In, 2 x 50 Ω Out
Connectors	2.4 mm, 3x Jack/Female
Dimensions	59.7 x 38.1 x 14 mm 2.35″ x 1.50″ x 0.55″
Weight	45.3 g (1.6 oz.)
Temperature Limits	-40° to +100° C, operating
RoHS Compliance	Made with lead-free solder
Warranty	1 year, see website

PRODUCT SUMMARY

The HL9405 is a signal splitter and combiner that offers industry-best amplitude and phase match over a bandwidth of 500 kHz to 50 GHz (-3 dB).

It is suitable for use in 40 Gbps communications systems, high-speed analog-to-digital conversion, frequency response testing for differential devices, and many other applications.

DEPLOYMENT NOTES

Although the HL9405 ports are labeled as RF In/Out, this device is bidirectional and can be used either as a signal splitter or combiner.

When the device is used as a signal combiner using differential signals with unmatched source impedance, attenuators (3-6 dB) may be required to improve isolation.

If the DC voltage of the input or output is not zero, DC block capacitors are required.

ADDITIONAL DATA

Higher-resolution versions of the charts on the following pages are available on our website, along with normal and mixed-mode S-parameter files for a typical device.

HL9405 Bandwidth (-3 dB)

Bandwidth for all HYPERLABS baluns is defined as the range of frequencies where insertion loss is within -3 dB of the reference level (-6 dB).

Figure 1 below shows better than -9 dB insertion loss up to 50 GHz when the device is used as a signal splitter.

HL9405 Amplitude Match

Amplitude match is a comparison between the signals on the RF Out +/- ports of a balun used as a signal splitter. This specification is derived from the insertion loss (in dB) measured on the output ports of the device.

Figure 1 below shows typical HL9405 insertion loss from 5 MHz to 50 GHz when the device is used as a signal splitter.

The amplitude balance can be seen by comparing the non-inverting output (blue trace), with the inverting output (red trace).

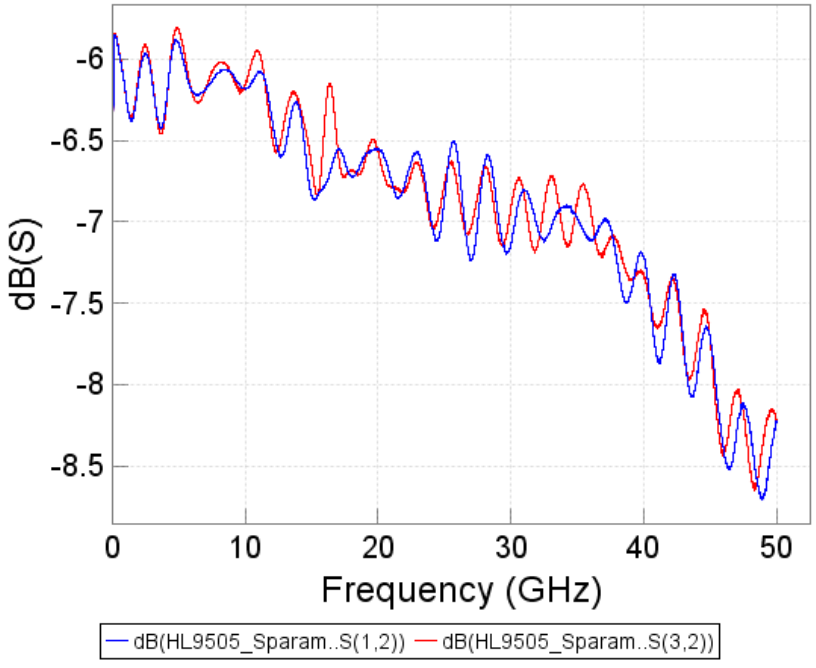

Figure 1: Typical insertion loss and amplitude match of the HL9405 RF Outputs when used as a signal splitter

When the HL9405 is used as a combiner, mixed mode parameters provide additional information on device performance.

For more on the HL9405 performance as a signal combiner, please see the section titled HL9405 Mixed Mode Data.

HL9405 Phase Match

The HL9405 is a 180° balun, so the phase match of the RF Out+ and RF Out- ports is specified in degrees from 180°.

Match is dependent on the delay of the output ports. For example, a 2° mismatch at 10 GHz requires the delay of each side of the balun to be within ≈ 0.5 ps of each other. Phase mismatch increases at higher frequencies.

Figure 2 below shows phase mismatch between the RF Outputs from 5 MHz to 50 GHz.

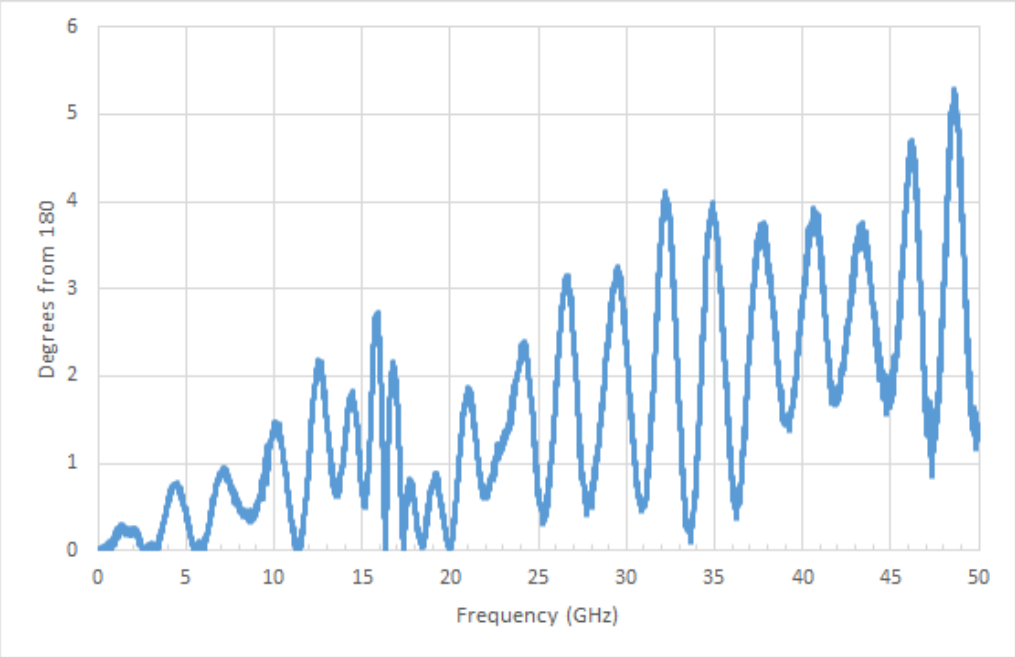

Figure 2: HL9405 phase match, represented as degrees from 180°

HL9405 Return Loss

Figure 3 shows the return loss on the HL9404 RF Input of a device used as a signal splitter. *Figure 4* shows the return loss on the RF Output+ port of a device used as a signal combiner. In both cases, bandwidth is from 5 MHz to 50 GHz.

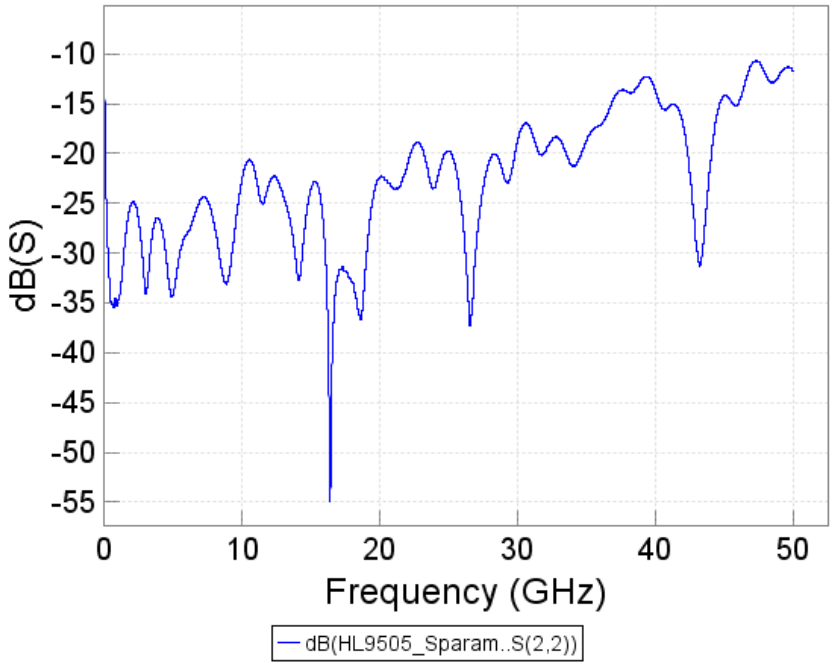

Figure 3: Typical return loss on the RF Input of the HL9405

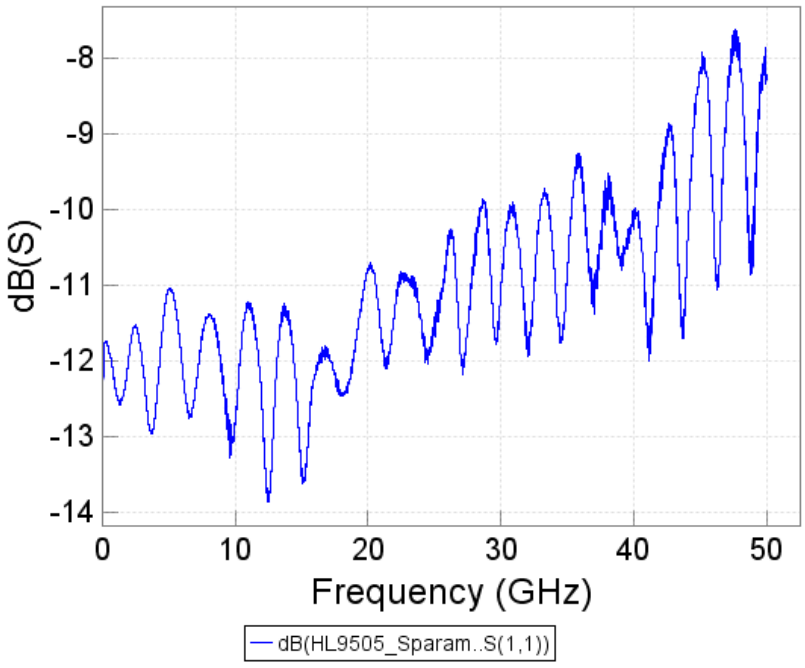

Figure 4: Typical return loss (S11) on the HL9405 RF Output+ ports

HL9405 VSWR

The typical Voltage Standing Wave Ratio (VSWR) of the HL9405 is shown in *Figure 5* below.

The blue and orange traces show typical VSWR on the RF In and RF Out+ ports, respectively.

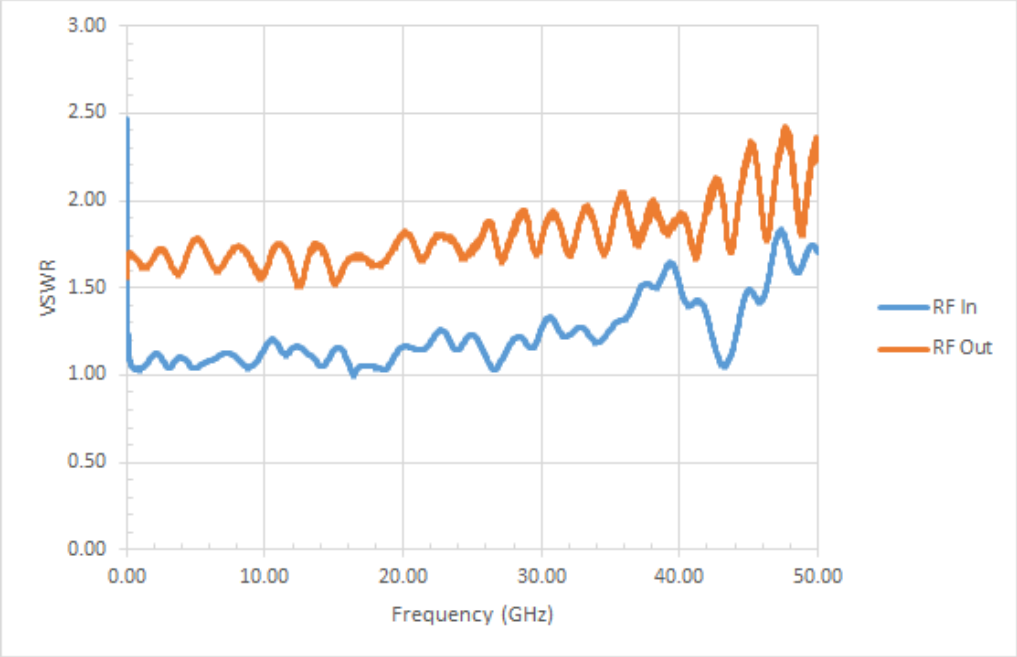

Figure 5: Typical VSWR on the HL9405 RF Input and RF Outputs

HL9405 CMRR

The exceptional Common Mode Rejection Ratio (CMRR) of the HL9405 allows it to be used as a signal combiner as well as a splitter.

Figure 6 shows the CMRR of the HL9405 when used to combine a differential signal from a 50 GHz VNA source.

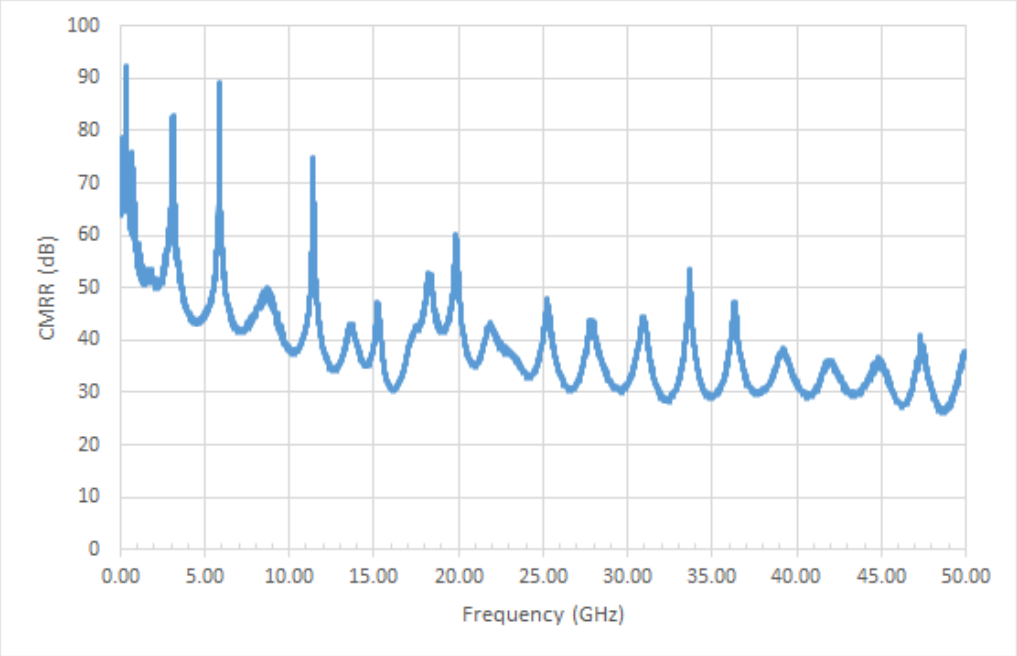

Figure 6: CMRR of HL9405 used as a signal combiner

HL9405 Mixed Mode Data

The design of HYPERLABS baluns allows the HL9405 to be used as a signal combiner as well as a signal splitter.

In combiner mode, the balun converts a differential source signal into a single-ended output, minimizing common mode noise and harmonic distortion.

In certain combiner applications using differential signals with unmatched source impedance, attenuators (3-6 dB) may be required to improve isolation between the differential ports. In applications with a well-matched differential input, or where the balun is used as a splitter, attenuators are not required.

HL9405 combiner performance is best characterized from mixed-mode S-parameters using a 4-port VNA as a differential source.

Figure 7 below shows the mixed-mode measurements of a typical HL9405 unit. Full mixed-mode data for the HL9405 can be found in the S-parameters file available on the HYPERLABS website.

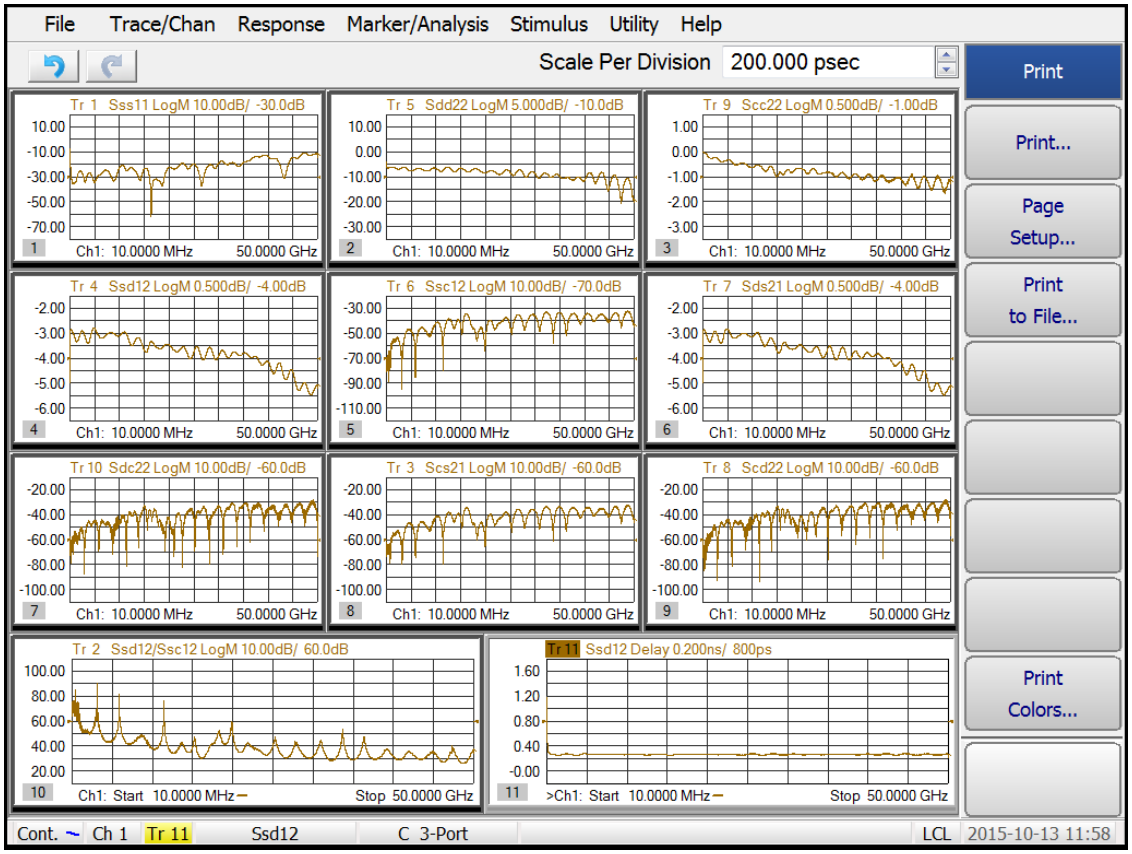

Figure 7: Mixed mode data for the HL9405 measured on a VNA with differential source to 50 GHz

HL9405 Eye Diagrams

The following pages contain pseudo-random binary sequence (PRBS) eye diagrams for the HL9405. Measurements were taken at 10 Gbps, using long (31-bit) and short (7-bit) patterns.

Figure 8: Gbps PRBS pattern as applied to the HL9405 RF In port

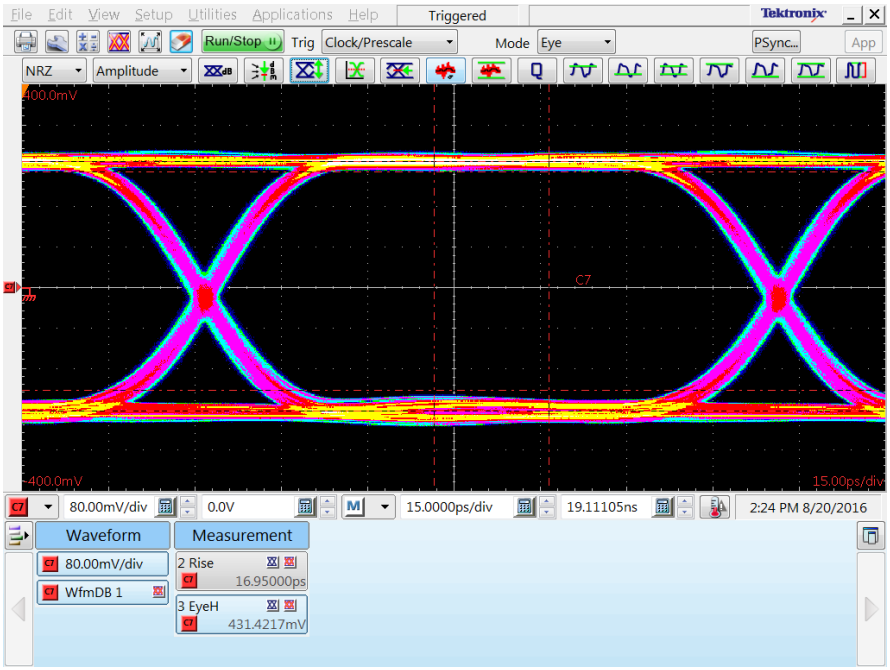

Figure 9: Eye diagram (10 Gbps, 7-bit pattern) of the HL9405 RF In port

HL9405 Eye Diagrams (cont.)

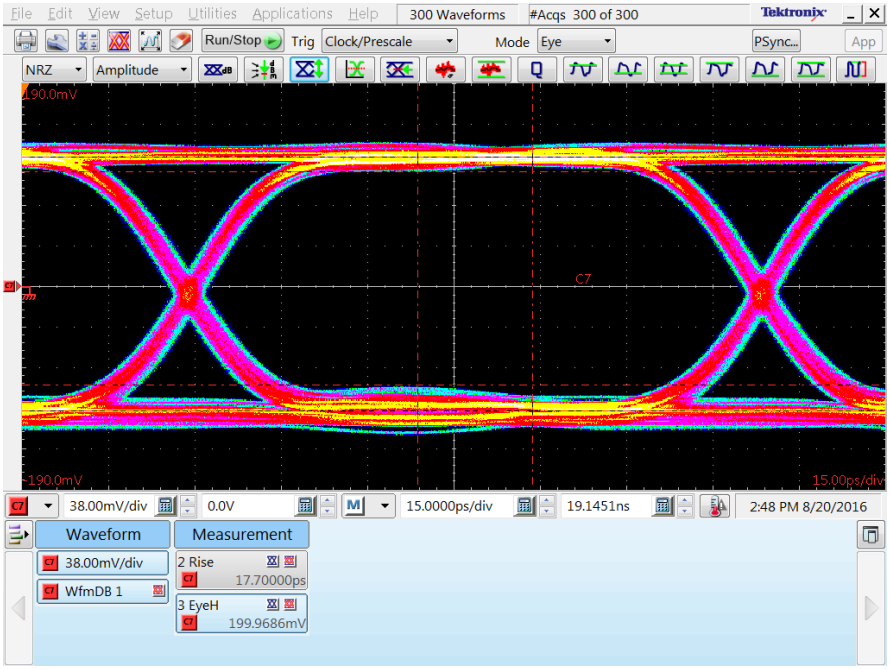

Figure 10: Eye diagram (10 Gbps, 7-bit pattern) of the HL9405 RF Out+ (non-inverting) port

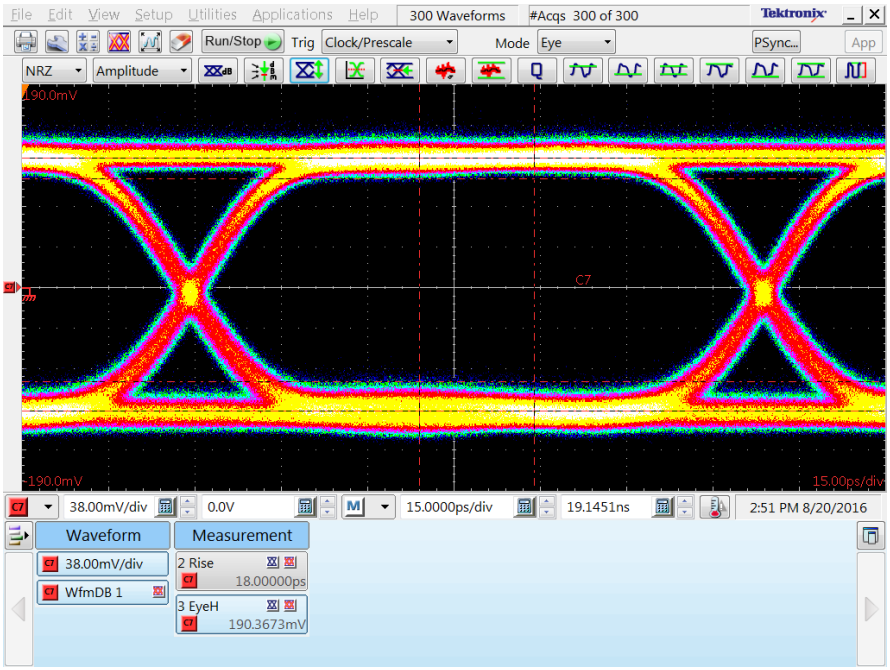

Figure 11: Eye diagram (10 Gbps, 31-bit pattern) of the HL9405 RF Out+ (non-inverting) port

HL9405 Eye Diagrams (cont.)

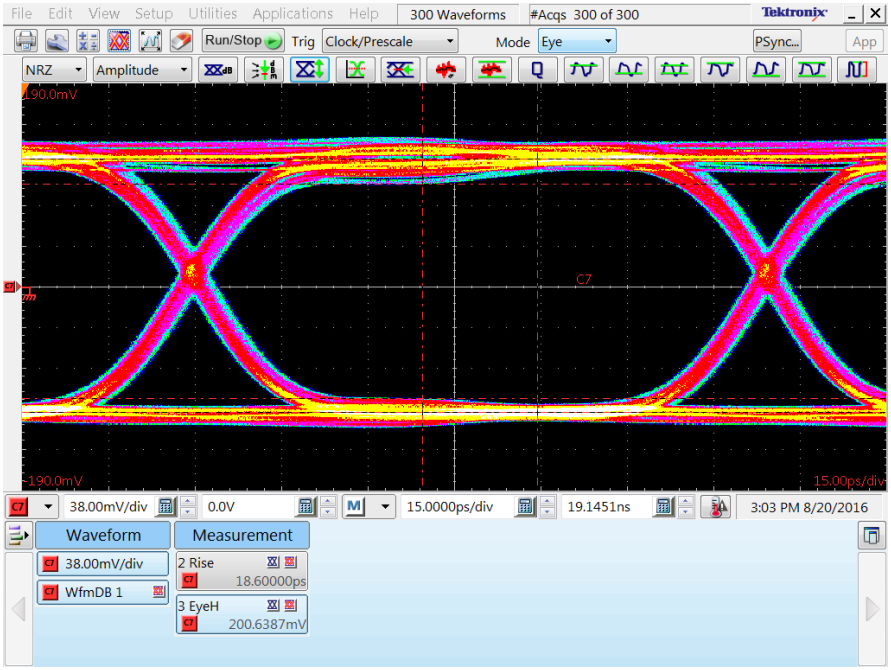

Figure 12: Eye diagram (10 Gbps, 7-bit pattern) of the HL9405 RF Out- (inverting) port

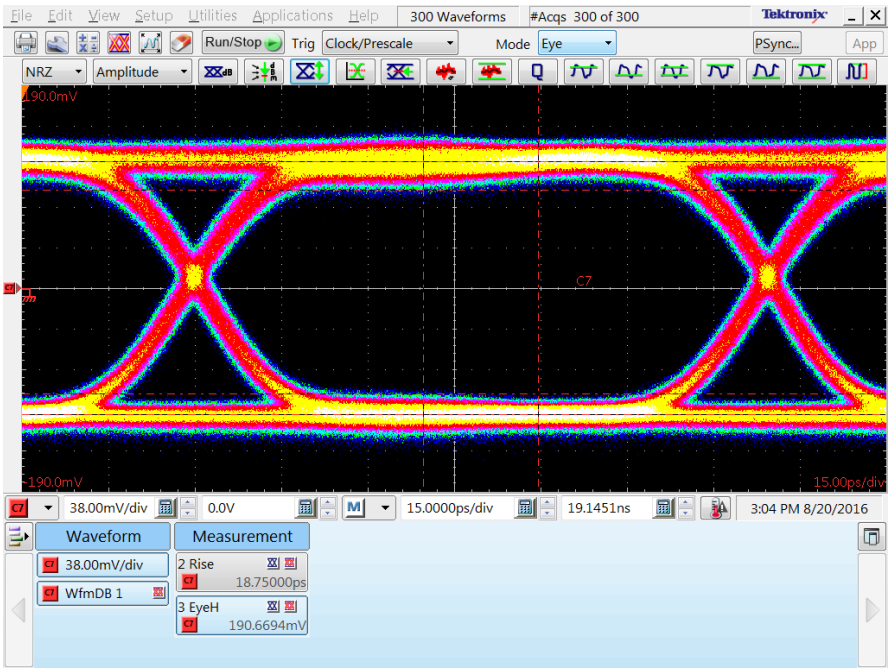

Figure 13: Eye diagram (10 Gbps, 31-bit pattern) of the HL9405 RF Out- (inverting) port